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erwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number Application Number 10/719,469 TRANSMITTAL Filing Date November 21, 2003 First Named Inventor **FORM** Vinod K. Balakrishnan Art Unit 2153 **Examiner Name** Philip J. Chea (to be used for all correspondence after initial filing) Attorney Docket Number Intel-050PUS Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication to TC Fee Transmittal Form Drawing(s) Appeal Communication to Board Licensing-related Papers Fee Attached of Appeals and Interferences Appeal Communication to TC ✓ Petition (Appeal Notice, Brief, Reply Brief) Amendment/Reply Petition to Convert to a Proprietary Information After Final Provisional Application Power of Attorney, Revocation Status Letter Affidavits/declaration(s) Change of Correspondence Address Other Enclosure(s) (please Identify Terminal Disclaimer Extension of Time Request below): Return postcard Request for Refund Express Abandonment Request CD, Number of CD(s) Information Disclosure Statement Landscape Table on CD Certified Copy of Priority Remarks Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name Daly, Crowley, Mofford & Durkee, LLP Signature Printed name Anthony Thoosey Date Reg. No. May 27, 2008 55.773 **CERTIFICATE OF TRANSMISSION/MAILING** I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below: Signature Date May 27, 2008 Typed or printed name

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Attorney's Docket No.: INTEL-050PUS

Intel Docket No. P17386

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vinod K. Balakrishnan Art Unit: 2153

Serial No.: 10/719,469 Examiner : Philip J. Chea Filed : November 21, 2003 Assignee : Intel Corporation

Conf. No.: 2596

Title : DYNAMIC RESOURCE ALLOCATION SYSTEMS AND METHODS

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.116 IN REPLY TO ACTION OF MARCH 27, 2008

In accordance with the provisions of 37 C.F.R. §1.116, please amend the above-identified application as follows: